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**BOX AF**

2811 AF  
AMENDMENT UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
GROUP 2811  
PATENT APPLICATION

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Yoko HORIGUCHI

Appln. No.: 09/615,705

Confirmation No.: Not Yet Assigned

Group Art Unit: 2811

Filed: July 13, 2000

Examiner: O. Nadav

For: SEMICONDUCTOR INTEGRATED CIRCUIT

**AMENDMENT UNDER 37 C.F.R. § 1.116**

**ATTN: BOX AF**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action dated August 15, 2001, please amend the above-identified application as follows:

**IN THE CLAIMS:**

**Please cancel claim 18 without prejudice or disclaimer.**

**Please add the following new claim:**

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20. (New) A semiconductor/integrated circuit device according to claim 1, wherein said electrostatic protection element is a diode, constituted by forming on a substrate or a well having a first conductive type two diffusion layers having a second conductive type, an opposite conductive type to the first conductive type, so as to closely face each other.

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